

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial Number 09/292,132
Filing Date April 14, 1999
Inventor Salman Akram et al.
Assignee Micron Technology, Inc.
Group Art Number 2812
Examiner S. Mulpuri
Attorney's Docket No. MI22-1171
Title: Methods of Forming a Transistor Gate

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether the submitted references are prior art.


Citation of these references is respectfully requested.

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Respectfully submitted,

Dated: 7-15-04

Attorney:


D. Brent Kenady
Reg. #40,045

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Practitioner's Docket No. MI22-1171

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Akram et al., Salman

Application No.: 09/292,132

Filed: 04/14/99

For: Methods of Forming a Transistor Gate

Group No.: 2812

Examiner: S. Mulpuri

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LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Salmon Abram et al.				
				FILING DATE April 14, 1999		GROUP 2812		
U.S. PATENT DOCUMENTS								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate	
	AA	5,554,871	09/10/96	Yamashita et al.				
	AB	5,726,479	03/10/98	Masumoto et al.				
	AC							
	AD							
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	AF							
	AG							
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FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AK	JP 9252117	09/22/97	Japan			Abs.	
	AL	JP 9074199	03/18/97	Japan			Abs.	
	AM	JP 8213605	08/20/96	Japan			Abs.	
	AN	JP 8181309	07/12/96	Japan			Abs.	
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	AQ	JP 5251463	09/28/93	Japan			Abs.	
	AR	JP 4062974	02/27/92	Japan			Abs.	
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
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